

Fig . 1

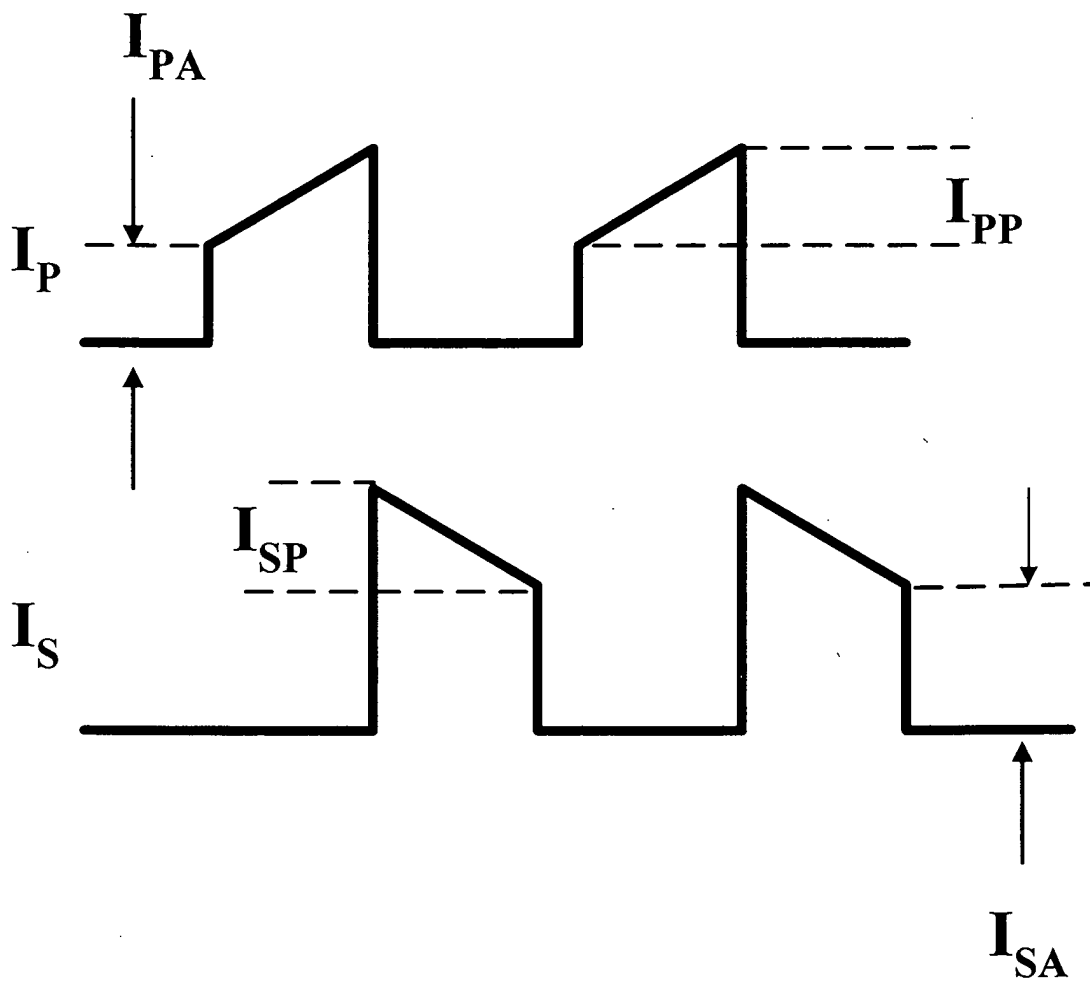


Fig . 2

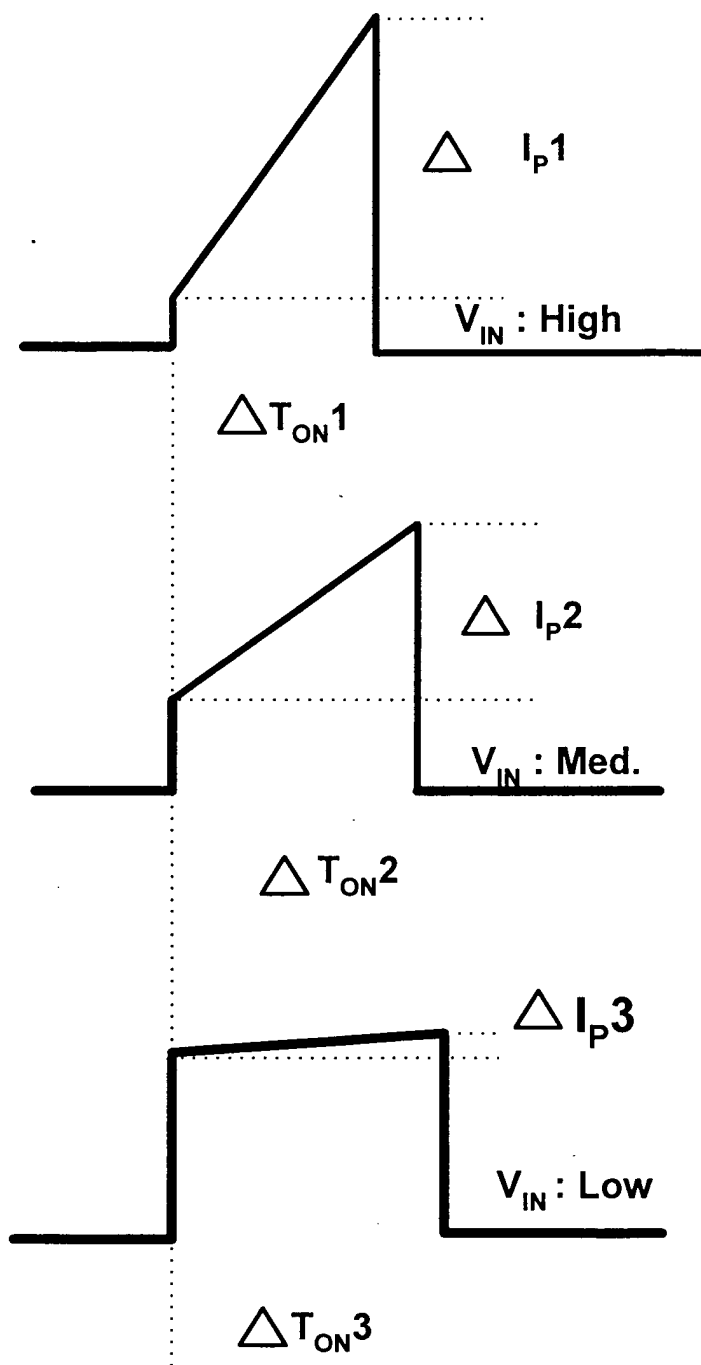


Fig . 3

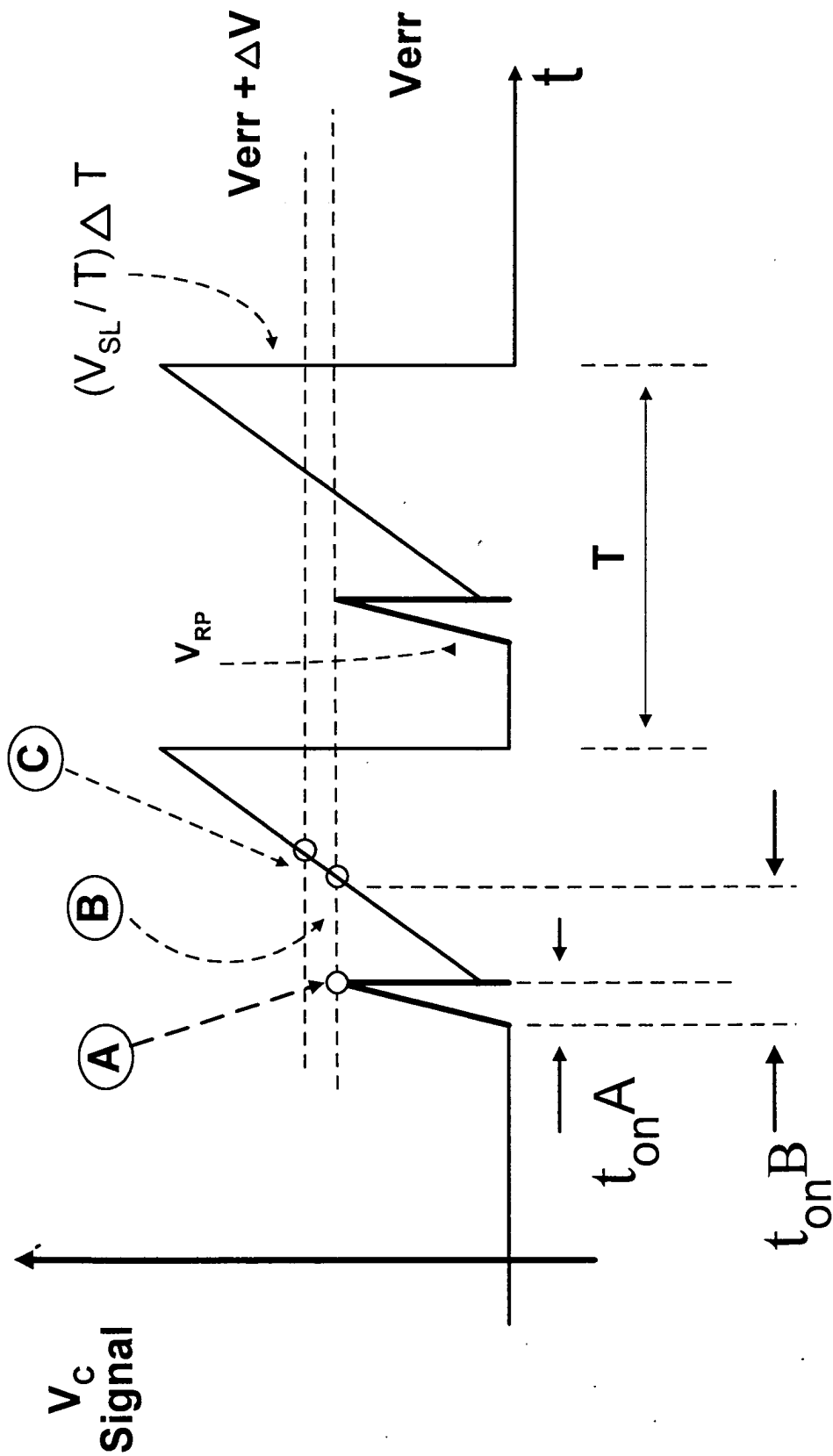


Fig . 4

Fig . 5

The diagram shows a precision rectifier circuit. The input V_{IN} is connected to a resistor network. A feedback loop is formed by a resistor and a capacitor. The output V_O is connected to a load resistor R_P . A reference voltage V_R is provided by a separate source. The circuit includes an operational amplifier (EA) and a latch (R, S, Q). The output of the latch is connected to the input of the operational amplifier. The output of the operational amplifier is connected to the output of the circuit. The circuit is labeled with various components and signals.

Fig . 6

The diagram illustrates a power MOSFET switching circuit with several integrated functional blocks and protection features. The input voltage V_{IN} is connected to the gate of the MOSFET Q_2 through a network of components including a capacitor C_{IN} , a transformer T_M , and a pulse generator 300 that produces a square wave V_{SW} . The MOSFET Q_2 is connected to a load resistor R_P and a sense resistor R_S . The output voltage V_O is taken across the load resistor. A feedback network is formed by a resistor R and a capacitor C_2 , which feeds into a feedback block 100 . This block contains a voltage divider (R_1, R_2, R_3), a MOSFET Q_1 , and capacitors C_1 and C_T . The feedback signal V_{FB} is derived from this network and is fed into an error amplifier EA (block 150). The error amplifier also receives a reference voltage V_R and a signal from a protection block 200 . The protection block 200 includes a diode D_1 , a resistor R_4 , and a diode D_T . A current sense block 250 is connected in series with the MOSFET Q_2 and the load resistor R_P . The current sense block contains a MOSFET Q_1 and a diode D_1 . The output of the current sense block is fed into the error amplifier EA . The error amplifier EA is also connected to a feedback network consisting of a resistor R and a capacitor C_2 . The error amplifier EA is connected to a feedback network consisting of a resistor R and a capacitor C_2 . The error amplifier EA is connected to a feedback network consisting of a resistor R and a capacitor C_2 .

Fig . 7

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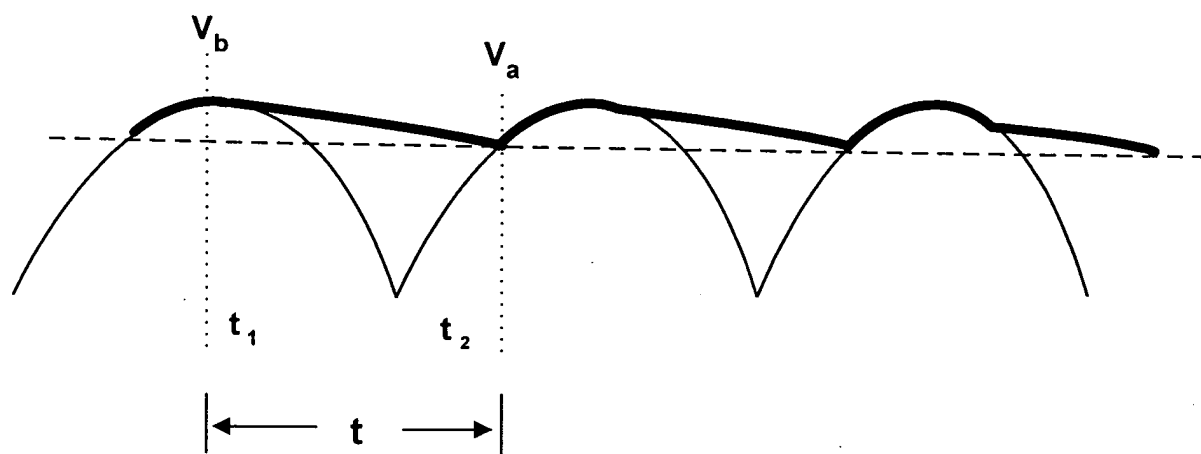


Fig . 8